

<b>Notice of References Cited</b>	Application/Control No. 10/087,304		Applicant(s)/Patent Under Reexamination STECHER, JOHN JOSEPH	
	Examiner Anil Khatri		Art Unit 2193	Page 1 of 1

#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
X	A	US-6,772,413	08-2004	Kuznetsov, Eugene	717/136
X	B	US-6,144,954	11-2000	Li, Chou H.	706/62
X	C	US-6,539,541	03-2003	Geva, Robert Y.	717/150
X	D	US-6,662,357	12-2003	Bowman-Amuah, Michel K.	717/120
X	E	US-6,070,009	05-2000	Dean et al.	717/130
X	F	US-6,549,930	04-2003	Chrysos et al.	718/104
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

#### FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Ramirez et al, "Code layout optimization for transaction processing workloads", IEEE, pp 155-164, 2001
	V	Hill et al, "ransfer optimization via simultaneous perturbation stochastic approximation", ACM Proc. of 1995 Winter Simulation Conf, pp 242-249, 1995
	W	Lempel et al, "Optimizing result prefetching in web search engines with segmented indicies", ACM Trans on internet tech. vol. 4, no. 1, pp 31-59, Feb. 2004
	X	Stimler et al, "A methodology for calculating and optimizing real time system performance", Comm. of the AC< vol. 11, no. 7, pp 509-516, July 1968

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.